

10/788550

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# BLAKELY SOKOLOFF TAYLOR & ZAFMAN

A LIMITED LIABILITY PARTNERSHIP INCLUDING LAW CORPORATIONS

TELEPHONE (310) 207-3800

FACSIMILE (310) 820-5988

(310) 820-5270

BSTZ\_MAIL@BSTZ.COM  
WWW.BSTZ.COM

INTELLECTUAL PROPERTY LAW

12400 WILSHIRE BOULEVARD  
SEVENTH FLOOR  
LOS ANGELES, CALIFORNIA 90025-1040

OTHER OFFICES

ORANGE COUNTY/COSTA MESA, CA  
SILICON VALLEY/SUNNYVALE, CA  
DENVER, CO  
PORTLAND/BEAVERTON, OR  
SEATTLE, WA

May 12, 2009

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**  
MAY 27 2009  
**of Correction**

Re: **CERTIFICATE OF CORRECTION**  
U. S. Letters Patent No.: 7,278,044 B2  
Issued: October 2, 2007  
For: Semiconductor Memory Device for Reducing Address Access Time  
Inventor: Ho-Uk Song  
Our File No.: 051876.P596

Dear Sir:

Enclosed is a Certificate of Correction (two copies) for the above-referenced patent.

This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Eric S. Hymn  
Reg. No. 30,139

ESH/ts

Enclosures  
PTO Form- 1449

**UNITED STATES PATENT AND TRADEMARK OFFICE**  
**CERTIFICATE OF CORRECTION**

**PATENT NO:** 7,278,044 B2

**DATED:** October 2, 2007

**INVENTOR(S):** Ho-Uk Song

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 14, Claim 11, line 13, please delete "one external clock period "

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**MAILING ADDRESS OF SENDER:**  
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP  
12400 Wilshire Blvd. 7th floor  
Los Angeles, CA 90025-1030

**PATENT NO.** 7,278,044 B2

**Certificate of Correction (PTO Form 1050)-Amended**

1 of 1

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